



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Kazuhiro ITOH et al.

Serial No.:

10/726,924

Filed: December 03, 2003

For:

Input/Output Coupling Structure for Dielectric

Waveguide Resonator

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 Examiner: Group Art: 2816

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March 31, 2004 (Date of Deposit)

Thomas Langer

Signature March 31, 2004 Date of Signature

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SIR:

In compliance with the duty of disclosure under 37 C.F.R. § 1.56 and in accordance with the practice under 37 C.F.R. §§ 1.97 and 1.98, the Examiner's attention is directed to the documents listed on the enclosed Form PTO/SB/08A. Copies of the listed documents are also enclosed.

It is respectfully requested that the above information be considered by the Examiner and that the copy of the enclosed Form PTO-1449 be returned indicating that such information has been considered.

In accordance with 37 C.F.R §§1.97(g) and (h), the filing of this Information Disclosure Statement should not be construed as a representation that a search has been made or that information cited is, or is considered to be, material to patentability as defined in §1.56(b), or that any cited document listed or attached is (or constitutes) prior art. Unless otherwise indicated, the date of publication indicated for an item is taken from the face of the item and Applicant(s) reserve(s) the right to prove that the date of publication is in fact different.

If any fees or charges are deemed required at this time in connection with the application, the same may be charged to our Patent and Trademark Office Deposit Account No. 03-2412.

Respectfully submitted, COHEN, PONTANI, LIEBERMAN & PAVANE

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Dated: March 31, 2004

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INFORMATION DISCLOSURE
STATEMENT BY APPLICANT
(Use as many sheets as necessary)

Application of the property o

of

Sheet

Complete if Known			
Application Number	10/726,924		
Filing Date	December 03, 2003		
First Named Inventor	Kazuhiro ITOH		
Art Unit			
Examiner Name			
Attorney Docket Number	5267-74		

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number  Number-Kind-Code <sup>2 (if</sup>	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant
		Number-Kind-Code - Kinown)			Figures Appear
		US-5,926,079	07/20/1999	Heine et al.	
		US-6,020,800	02/01/2000	Arakawa et al.	
		US-			
		US-			
		US-		7. 7	
		US-			

	FOREIGN PATENT DOCUMENTS					
Examiner	Cite	Foreign Patent Document	Publication Date	Name of Patentee or	Pages, Columns, Lines,	T <sup>6</sup>
Initials*	No. 1	Country Code <sup>3-</sup> Number <sup>4-</sup> Kind-Code <sup>5</sup> (if known)	MM-DD-YYYY	Applicant of Cited Document	Where Relevant Passages or Relevant Figures Appear	
		JP 2002-208806	07/26/2002	Mitsubishi Electric Corp.		Abstract
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Form 1449/PTO

## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet	2	of	2	

Complete if Known			
Application Number	10/726,924		
Filing Date	December 03, 2003		
First Named Inventor	Kazuhiro ITOH		
Art Unit			
Examiner Name			
Attorney Docket Number	5267-74		

Examiner Initials*	Cite	NON PATENT LITERATURE DOCUMENTS  Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of	T
	No. 1	the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	
		Dominic Deslandes et al., "Integrated Microstrip and Rectangular	_
		Waveguide in Planar Form", IEEE Microwave and Wireless Components	
		Letters, Vol. 11, No. 2, pp. 68-70, February 2001	
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